L Number	Hits	Search Text	DB	Time stamp
7	0	((processor or multiprocessor or microprocessor) near3 (contain$3 or hold$3 or includ$3 or hav$3 or has)) near3 ((MOS and transistor) and gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 15:16
8	205	((processor or multiprocessor or microprocessor or semiconductor or chip or (integrated adj circuit)) near3 (contain$3 or hold$3 or includ$3 or hav$3 or has)) near3 (MOS and transistor and gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 15:21
9	0	((processor or multiprocessor or microprocessor or semiconductor or chip or (integrated adj circuit)) near3 (contain$3 or hold$3 or includ$3 or hav$3 or has)) near3 (p-MOS and n-MOS and transistor and gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 15:18
10	1	((processor or multiprocessor or microprocessor or semiconductor or chip or (integrated adj circuit)) near3 (contain$3 or hold$3 or includ$3 or hav$3 or has)) near3 (MOS and MOS and transistor and gate) and 716/$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 15:20
11	1	((processor or multiprocessor or microprocessor or semiconductor or chip or (integrated adj circuit) or IC) near3 (contain$3 or hold$3 or includ$3 or hav$3 or has)) near3 (MOS and MOS and transistor and gate) and 716/$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 15:20
12	210	((processor or multiprocessor or microprocessor or semiconductor or chip or (integrated adj circuit) or IC) near3 (contain$3 or hold$3 or includ$3 or hav$3 or has)) near3 (MOS and transistor and gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 15:22
13	29	((processor or multiprocessor or microprocessor or semiconductor or chip or (integrated adj circuit) or IC) near3 (contain$3 or hold$3 or includ$3 or hav$3 or has)) near3 (MOS and transistor and gate) and symmetric$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 16:20
14	0	(MOS adj transostor) same (width and length)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 16:21
15	2428	(MOS adj transistor) same (width and length)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 16:21
16	222	((MOS adj transistor) same (width and length)) and processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 16:21
17	3	((MOS adj transistor) same (width and length)) same processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 16:28
28	63	((MOS adj transistor) same (width and length)) same (orient$4 or plac$3 or orientation)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 16:31
29	8	(((MOS adj transistor) same (width and length)) same (orient$4 or plac$3 or orientation)) and symmetric$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 16:31

-	2	5787310.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/15 14:43
-	0	((on adj chip adj processor) same layout) and ((multiple or plurality) near3 processor) and symmetric$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/15 14:46
-	0	((on adj chip near3 processor) same layout) and ((multiple or plurality) near3 processor) and symmetric$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/15 14:46
-	0	(((on adj chip) near3 processor) same layout) and ((multiple or plurality) near3 processor) and symmetric$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/15 14:46
-	0	(floorplan near3 multiprocessor) and (layout near3 (multuple adj processor))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/15 14:51
-	70	(chip same layout) and ((multiple or plurality) near3 processor) and symmetric$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 11:12
-	0	(chip same layout) and ((multiple or plurality) near3 processor) and symmetric$3 and (linear adj axis)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 07:54
-	0	(multiprocessor same chip) with (layout and symmetric$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 07:56
-	1174	(multiprocessor same chip)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 07:56
-	30	(multiprocessor same chip) and layout and symmetric$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 12:08
-	24	(multiprocessor same chip) and ((layout or architecture) near3 symmetric$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 09:37
-	2	(shar$3 same center same symmetric$4) same (chip near3 (architecture or layout))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 09:40
-	4	(multiprocessor same chip) and layout and symmetric$4 and (CPU near3 (architecture or layout))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 12:41

-	15	multiprocessor and layout and symmetric$4 and (CPU near3 (architecture or layout))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 12:44
-	15	multiprocessor and layout and symmetric$4 and (CPU near3 (architecture or layout)) and shar$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 12:44
-	14	multiprocessor and layout and symmetric$4 and (CPU near3 (architecture or layout)) and shar$3 and (center or central)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 12:47
-	20	multiprocessor and layout and (symmetric$4 or mirror) and (CPU near3 (architecture or layout)) and shar$3 and (center or central)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 13:38
-	20	multiprocessor and layout and (symmetric$4 or mirror) and (CPU near3 (architecture or layout)) and (shar$3 near3 componenet or element or unit) and (center or central)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 11:10
-	6	multiprocessor and layout and (symmetric$4 or mirror) and (CPU near3 (architecture or layout)) and (shar$3 near3 (componenet or element or unit)) and (center or central)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 13:39
-	2	6516442.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/16 14:22
-	4	multiprocessor and layout and (symmetric$4 or mirror) and (CPU near3 (architecture or layout)) and (shar$3 near3 componenet or element or unit) and (center or central) and PLL	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 11:10
-	11	(chip same layout) and ((multiple or plurality) near3 processor) and symmetric$3 and PLL	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 12:05
-	6	(chip same layout) and ((multiple or plurality) near3 processor) and symmetric$3 and PLL and recovery	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 12:19
-	2	5987590.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 12:22
-	244	processor same (PLL and cache)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 12:23
-	1	(processor same (PLL and cache)) and chip and (symmetric$3 near3 (layout or architecture))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 12:25

-	307915	(processor same (PLL and cache)) and chip symmetric$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 12:26
-	107	processor near3 (logical adj unit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 12:27
-	0	(processor near3 (logical adj unit)) same symmetric$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 12:42
-	1418	((processor or CPU) near3 (architecture or layout)) and symmetric$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 12:43
-	2	6176893.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 12:44
-	15	processor with (bump near3 array)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 14:18
-	0	(processor same (bump near3 array)) and (mask adj pattern)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 14:19
-	1	(processor same (bump near3 array)) and mask	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 14:50
-	8337	processor same mask	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 14:50
-	4738	processor with mask	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 14:51
-	261	processor with (mask near3 pattern)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 14:55
-	0	(each adj processor) with (mask near3 pattern)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 15:26
-	0	(each adj (processor or chip or IC)) with (mask near3 pattern)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 14:52

-	0	(each adj (processor or chip or IC)) with mask	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 14:52
-	1	(each adj (processor or chip or IC)) with pattern	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 14:52
-	155	processor with (mask adj pattern)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 14:55
-	49	processor near3 (mask adj pattern)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 14:56
-	464	(processor or chip or IC) near3 (mask adj pattern)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 15:24
-	0	mask adj pattern adj for adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 15:25
-	0	(each adj processor) same (mask near3 pattern)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 15:26
-	0	(each adj processor) same mask	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 15:42
-	0	("each" adj processor) same mask	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/17 15:42
-	44	processor same (mask adj "2")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 10:18
-	0	mask adj pattern adj for adj (processor or IC or semiconductor or chip or device or (integrated adj circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 10:20
-	2943	(mask adj pattern) near3 (processor or IC or semiconductor or chip or device or (integrated adj circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 10:20
-	379	(mask adj pattern) near3 (processor or IC or semiconductor or chip or device or (integrated adj circuit)) and ((mask adj pattern) near3 second)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 10:21

-	270	(mask adj pattern) near3 (processor or IC or semiconductor or chip or device or (integrated adj circuit)) and ((mask adj pattern) near3 second) and manufactur$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 10:22
-	57	(mask adj pattern) near3 (processor or IC or semiconductor or chip or device or (integrated adj circuit)) and ((mask adj pattern) near3 second) and manufactur$3 and (pattern near3 (wiring or connect$3 or intercoinnect$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 11:16
-	0	processor same (p-MOS and n-MOS and gate near3 (source and drain))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 11:18
-	3	processor same (p-MOS and n-MOS and gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 11:26
-	0	(processor near3 (contain$3 or hold43 or includ43)) near3 (p-MOS and n-MOS and gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 11:29
-	0	processor near3 (p-MOS and n-MOS and gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 11:28
-	4371	processor near3 (transistor or p-MOS or n-MOS or gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 11:29
-	56	(processor near3 (contain$3 or hold43 or includ43)) near3 (transistor or p-MOS or n-MOS or gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 11:30
-	0	(processor near3 (contain$3 or hold43 or includ43)) near3 (transistor or p-MOS or n-MOS or gate) and 716/$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 11:39
-	0	(processor near3 (contain$3 or hold43 or includ43)) near3 (transistor and gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 11:39
-	0	(processor near3 (contain$3 or hold43 or includ43)) near3 (MOS and gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 11:39
-	0	((processor or multiprocessor) near3 (contain$3 or hold43 or includ43)) near3 (MOS and gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 11:42
-	0	((processor or multiprocessor or microprocessor) near3 (contain$3 or hold43 or includ43)) near3 (MOS and gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 11:42

-	0	((processor or multiprocessor or microprocessor) near3 (contain$3 or hold43 or includ$3 or hav$3 or has)) near3 (MOS and gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 11:42
-	0	((processor or multiprocessor or microprocessor) near3 (contain$3 or hold$3 or includ$3 or hav$3 or has)) near3 (MOS and gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 11:43
-	7	((processor or multiprocessor or microprocessor) near3 (contain$3 or hold$3 or includ$3 or hav$3 or has)) near3 ((MOS or transistor) and gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 15:16